

ABSTRACT OF THE DISCLOSURE

In a color separator circuit, edge detection filters detect an edge encountered in the horizontal direction. When the image signals corresponding to the thus detected edge are processed, selector circuits 12a to 12c select outputs from small-tap-number color separation filters 8a to 8c and 10a to 10c, respectively. When the image signals corresponding to the remaining portion other than the edge are processed, the selector circuits 12a to 12c select outputs from large-tap-number color separation filters 9a to 9c and 11a to 11c, respectively. A ratio calculator circuit 16 calculates, from the luminance signals fed from adder circuits 13a to 13c, the value by which a multiplier circuit 18 performs multiplication. The multiplier circuit 18 multiplies, by this value calculated by the ratio calculator circuit 16, the color separation signals added together by an adder circuit 17 so that, of all the color separation signals of which there are two kinds, those of one kind are vertically interpolated.